

	Hits	Search Text	DBs	Time Stamp
1	8232	((integrated adj circuit) IC) near2 test\$3	USPAT	2005/02/04 13:59
2	1	((integrated adj circuit) IC) near2 test\$3) same (record\$3 near2 (pass fail))	USPAT	2005/02/04 14:22
3	3	((integrated adj circuit) IC) near2 test\$3) same (timing adj margin)	USPAT	2005/02/04 14:21
4	0	((integrated adj circuit) IC) near2 test\$3) same (timing adj window)	USPAT	2005/02/04 14:09
5	38	((integrated adj circuit) IC) near2 test\$3) same (reference adj clock)	USPAT	2005/02/04 14:20
6	29	((integrated adj circuit) IC) near2 test\$3) same (reference adj clock)	US-PGPUB; EPO; JPO; IBM_TDB	2005/02/04 14:20
7	8	((integrated adj circuit) IC) near2 test\$3) same (timing adj margin)	US-PGPUB; EPO; JPO; IBM_TDB	2005/02/04 14:21
8	0	713/500.ccls and S1	USPAT	2005/02/04 14:26
9	28	713/500.ccls. and S1	USPAT	2005/02/04 14:27
10	484	714/724.ccls. and S1	USPAT	2005/02/04 14:28
11	0	714/724.ccls. and S1 and S2	USPAT	2005/02/04 14:30
12	0	714/724.ccls. and S1 and S3	USPAT	2005/02/04 14:28
13	2	714/724.ccls. and S1 and S5	USPAT	2005/02/04 14:29
14	1	714/731.ccls. and S1 and S5	USPAT	2005/02/04 14:29
15	0	714/731.ccls. and S1 and S2	USPAT	2005/02/04 14:30
16	1	714/733.ccls. and S1 and S2	USPAT	2005/02/04 14:30
17	0	714/742.ccls. and S1 and S2	USPAT	2005/02/04 14:30
18	2	714/742.ccls. and S1 and S5	USPAT	2005/02/04 14:31
19	1	(702/117,118,125).ccls. and S1 and S5	USPAT	2005/02/04 14:32

	Hits	Search Text	DBs	Time Stamp
20	0	(702/117,118,125).ccls. and S1 and S2	USPAT	2005/02/04 14:32
21	0	324/765.ccls. and S1 and S2	USPAT	2005/02/04 14:32
22	6	324/765.ccls. and S1 and S5	USPAT	2005/02/04 14:33
23	0	257/48.ccls. and S1 and S5	USPAT	2005/02/04 14:34
24	0	257/48.ccls. and S1 and S2	USPAT	2005/02/04 14:34
25	164	257/48.ccls. and S1	USPAT	2005/02/04 14:34
26	1	257/48.ccls. and S1 and S3	USPAT	2005/02/04 14:34
27	5	(Keith with dow).in.	USPAT	2005/02/04 15:26